CHI MEI OPTOELECTRONICS CORP.

Issued Date: Aug. 30, 2002
Model No.: N141X5 -L03
Approval

TFT LCD Approval Specification

MODEL NO.: N141X5 -L03

Customer : FUJITSU LIMITED

Approved by : _____

Note :

Liquid Crystal Display Division		
QRA Dept.	PDD II Dept.	PD Dept.
Approval	Approval	Approval
陳 91.9.3 永一	91.9.3 忠光	

CHI MEI OPTOELECTRONICS CORP.

Issued Date: Aug. 30, 2002
Model No.: N141X5 -L03
Approval

- CONTENTS -

REVISION HISTORY

Version	Date	Page (New)	Section	Description
Ver 0.0	Oct. 29,'01	All	All	Tentative Specification was first issued.
Ver 1.0	Jan. 10. '02	All	All	Preliminary Specification was first issued.
Ver 2.0	Apr. 30. '02	All	All	Approval Specification was first issued
Ver 3.0	Jun. 05, '02	All	All	Approval Specification for FJ was first issued
Ver 3.1	Aug. 12, '02	4	1.5	Add connector mounting position
		5	2.1	Add LCD cell life time
		7	3.1	Modify ripple voltage 100 (Typ.) → 100 (Max.)
		8	3.2	Modify lamp current : 3.5 (min) → 2.0 (min)
		16	5.6	Add EDID signal specification
		18	6.1	Modify input signal timing specification table. Add item Vsync frequency 70Hz (Max) Hsync frequency 56KHz (Max) Data enable pulse width 100 (min) Modify frame frequency 771 (min) → 770 (min) 812 (Max) → 1000 (Max) Modify one line scanning time 1340 (min) → 1180 (min) 1366 (Max) → 2000 (Max)
		20	7.2	Add color gamut : 45% (min) 50% (Typ)
		23		Add Note (8)
		26	9.2	Modify pallet packing drawing
Ver 3.2	Aug. 30, '02	16	5.6	Modify EDID signal specification
		17	5.6	Add high speed operation mode
		23	7.2	Modify Note (8)

1. GENERAL DESCRIPTION

1.1 OVERVIEW

N141X5 -L03 is a 14.1" TFT Liquid Crystal Display module with single CCFL Backlight unit and 20 pins LVDS interface. This module supports 1024 x 768 XGA mode and can display 262,144 colors. The optimum viewing angle is at 6 o'clock direction. The inverter module for Backlight is not built in.

1.2 FEATURES

- Thin and light weight
- XGA (1024 x 768 pixels) resolution
- DE (Data Enable) only mode
- 3.3V LVDS (Low Voltage Differential Signaling) interface with 1 pixel/clock
- SPWG (Standard Panel Working Group) Style A compatible

1.3 APPLICATION

- TFT LCD Notebook

1.4 GENERAL SPECIFICATI0NS

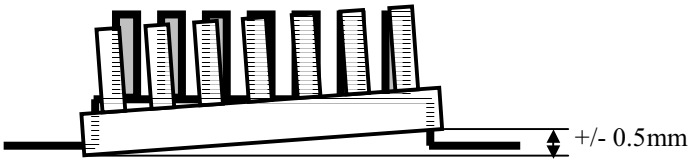
Item	Specification	Unit	Note
Active Area	285.7 (H) x 214.3 (V) (14.1" diagonal)	mm	(1)
Bezel Opening Area	289.3 (H) x 217.9 (V)	mm	
Driver Element	a-si TFT active matrix	-	-
Pixel Number	1024 x R.G.B. x 768	pixel	-
Pixel Pitch	0.279 (H) x 0.279 (V)	mm	-
Pixel Arrangement	RGB vertical stripe	-	-
Display Colors	262,144	color	-
Transmissive Mode	Normally white	-	-
Surface Treatment	Hard coating (3H), Anti-glare (Haze 35.5%), Surface reflection (Max. 2.0%)	-	-

1.5 MECHANICAL SPECIFICATIONS

Item		Min.	Typ.	Max.	Unit	Note
Module Size	Horizontal(H)	298.5	299.0	299.5	mm	(1)
	Vertical(V)	227.5	228.0	228.5	mm	
	Depth(D)	-	5.7	6.0	mm	
Weight		-	480	500	g	-
I/F connector mounting position		The mounting inclination of the connector makes the screen center within ±0.5mm as the horizontal.				(2)

Note (1) Please refer to the attached drawings for more information of front and back outline dimensions.

(2) Connector mounting position



2. ABSOLUTE MAXIMUM RATINGS

2.1 ABSOLUTE RATINGS OF ENVIRONMENT

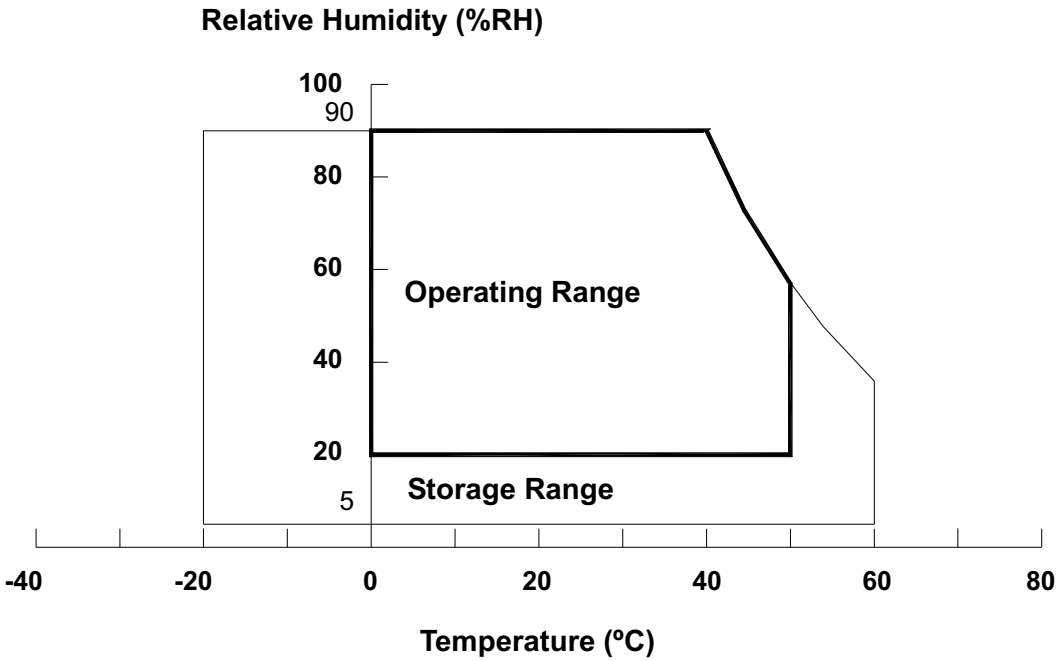
Item	Symbol	Value		Unit	Note
		Min.	Max.		
Storage Temperature	T_{ST}	-20	+60	ºC	(1)
Operating Ambient Temperature	T_{OP}	0	+50	ºC	(1), (2)
Shock (Non-Operating)	S_{NOP}	-	200	G	(3), (5)
Vibration (Non-Operating)	V_{NOP}	-	2	G	(4), (5)
LCD Cell Life Time	L_{CELL}	50000	-	Hrs	MTBF based

Note (1) Temperature and relative humidity range is shown in the figure below.

(a) 90 %RH Max. (Ta ≦ 40 ºC).

(b) Wet-bulb temperature should be 39 ºC Max. (Ta > 40 ºC).

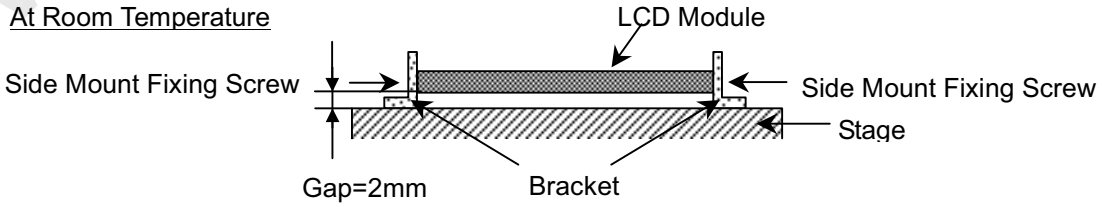
(c) No condensation .



Note (2) The temperature of panel surface should be 0 ºC Min. and 50 ºC Max.

Note (3) 2ms, half sine wave, 1 time for ± X, ± Y, ± Z.

Note (4) 10 ~ 500 Hz, 0.5 Hr / Cycle, 4 cycles for each X, Y, Z. The fixing condition is shown as below:



Note (5) At testing Vibration and Shock, the fixture in holding the module has to be hard and rigid enough so that the module would not be twisted or bent by the fixture.

2.2 ELECTRICAL ABSOLUTE RATINGS

2.2.1 TFT LCD MODULE

Item	Symbol	Value		Unit	Note
		Min.	Max.		
Power Supply Voltage	Vcc	-0.3	+4.0	V	(1)
Logic Input Voltage	V_{IN}	-0.3	Vcc+0.3	V	

2.2.2 BACKLIGHT UNIT

Item	Symbol	Value		Unit	Note
		Min.	Max.		
Lamp Voltage	V_L	-	2.5K	V_{RMS}	(1), (2), I_L = (6.0) mA
Lamp Current	I_L	-	7.0	mA_{RMS}	(1), (2)
Lamp Frequency	F_L	-	85	KHz	

Note (1) Permanent damage to the device may occur if maximum values are exceeded. Function operation should be restricted to the conditions described under Normal Operating Conditions.

Note (2) Specified values are for lamp (Refer to Section 3.2 for further information).

3. ELECTRICAL CHARACTERISTICS

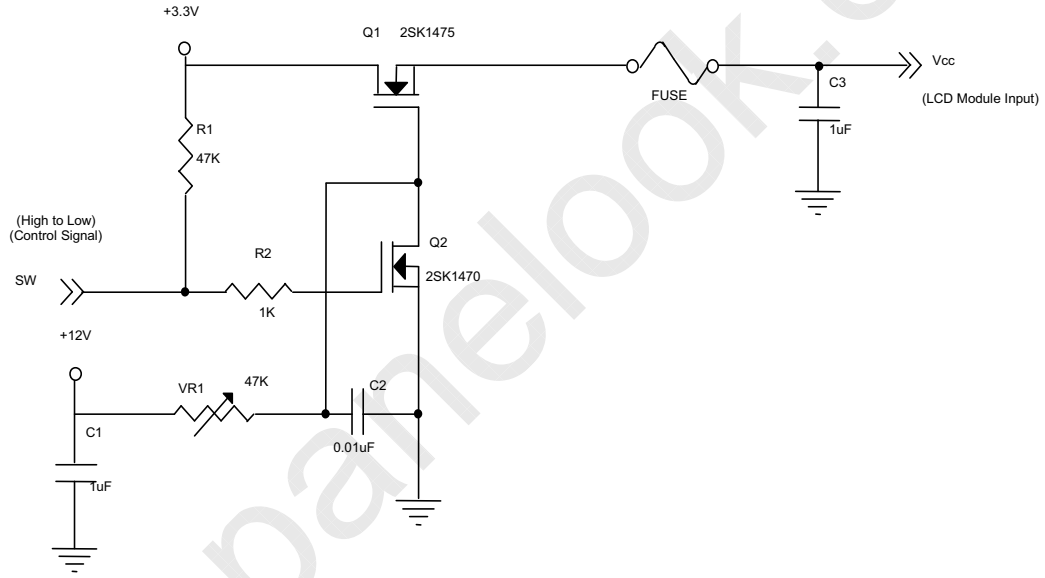
3.1 TFT LCD MODULE

Ta = 25 ± 2 ºC

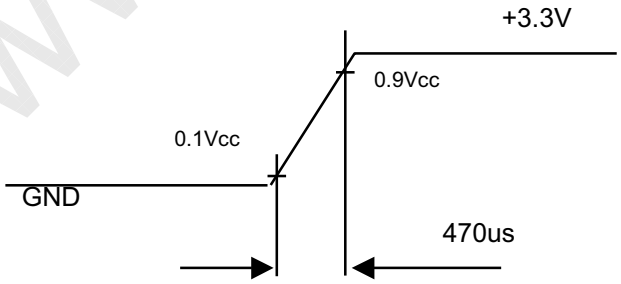
Parameter		Symbol	Value			Unit	Note
			Min.	Typ.	Max.		
Power Supply Voltage		Vcc	3.0	3.3	3.6	V	-
Ripple Voltage		V_{RP}	-	-	100	mV	-
Rush Current		I_{RUSH}	-	0.85	1.5	A	(2)
Power Supply Current	White	Icc	-	300		mA	(3)a
	Black		-	500		mA	(3)b
	Vertical Stripe		-	475		mA	(3)c
Differential Input Voltage for LVDS Receiver Threshold	"H" Level	V_{IH}	-	-	+100	mV	-
	"L" Level	V_{IL}	-100	-	-	mV	-
Terminating Resistor		R_T	-	100	-	Ohm	-

Note (1) The module should be always operated within above ranges.

Note (2) Measurement Conditions:

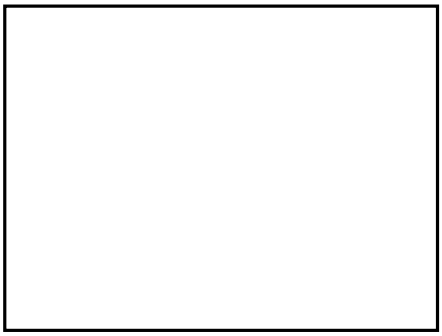


Vcc rising time is 470us



Note (3) The specified power supply current is under the conditions at Vcc = 3.3 V, Ta = 25 ± 2 ºC, DC Current and f_v = 60 Hz, whereas a power dissipation check pattern below is displayed.

a. White Pattern

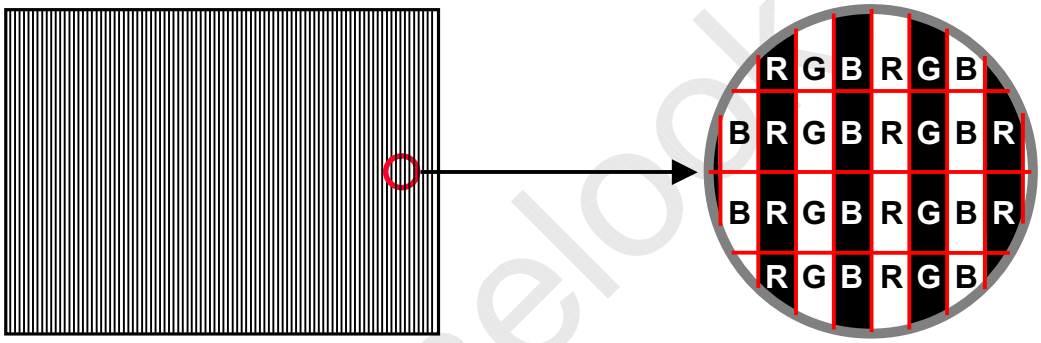


Active Area

b. Black Pattern

Active Area

c. Vertical Stripe Pattern



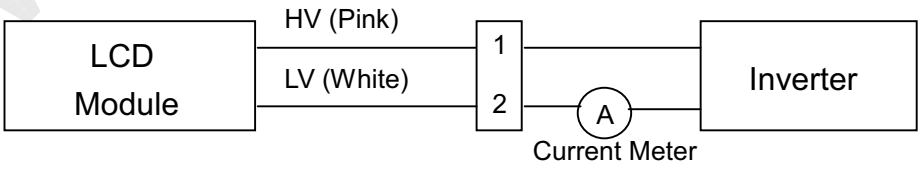
Active Area

3.2 BACKLIGHT UNIT

Ta = 25 ± 2 ºC

Parameter	Symbol	Value			Unit	Note
		Min.	Typ.	Max.		
Lamp Input Voltage	V_L	585	650	715	V_{RMS}	I_L = 6.0 mA
Lamp Current	I_L	2.0	6.0	6.5	mA_{RMS}	(1)
Lamp Turn On Voltage	V_S	-	-	1130 (25 °C)	V_{RMS}	(2)
		-	-	1315 (0 °C)	V_{RMS}	(2)
Operating Frequency	F_L	40	-	80	KHz	(3)
Lamp Life Time	L_{BL}	10,000	-	-	Hrs	(5)
Power Consumption	P_L	-	3.9	-	W	(4), I_L = 6.0 mA

Note (1) Lamp current is measured by utilizing a high frequency current meter as shown below:



Note (2) The voltage shown above should be applied to the lamp for more than 1 second after startup. Otherwise the lamp may not be turned on.

Note (3) The lamp frequency may generate interference with horizontal synchronous frequency from the display, and this may cause line flow on the display. In order to avoid interference, the lamp frequency should be detached from the horizontal synchronous frequency and its harmonics as far as possible.

Note (4) $P_L = I_L \times V_L$

Note (5) The lifetime of lamp is defined as the time when it continues to operate under the conditions at Ta = 25 ±2 °C and I_L = 6.0 mA_{RMS} until one of the following events occurs:

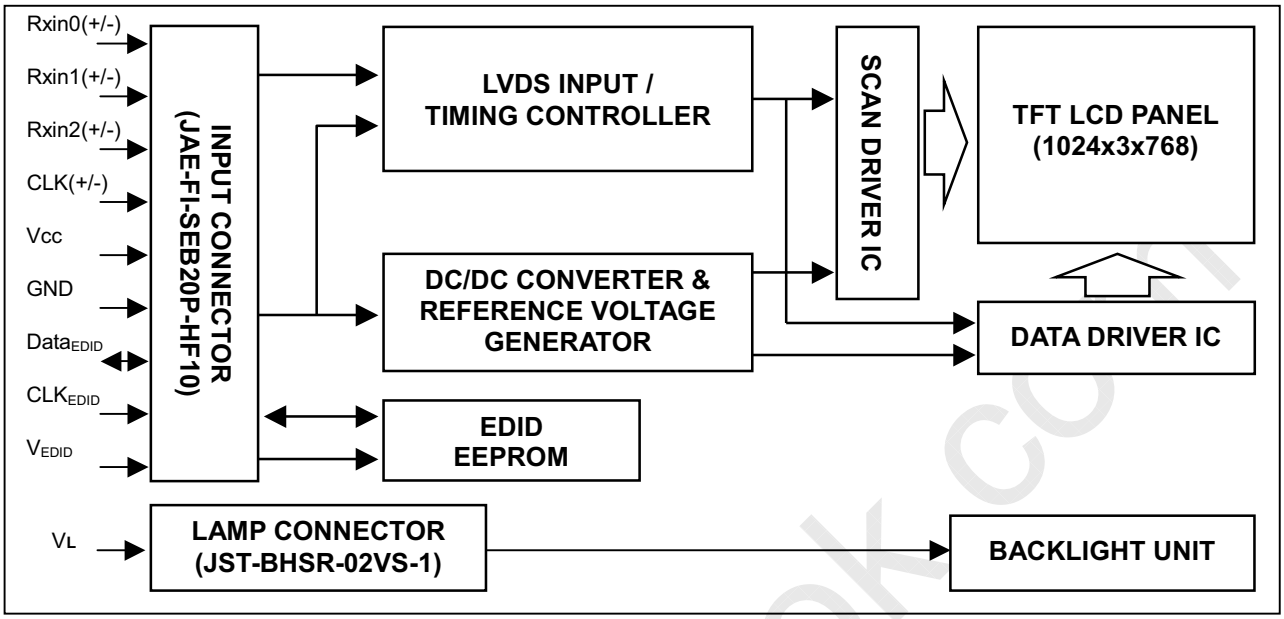
(a) When the brightness becomes ≦ 50% of its original value.

(b) When the effective ignition length becomes ≦ 80% of its original value. (Effective ignition length is defined as an area that the brightness is less than 70% compared to the center point.)

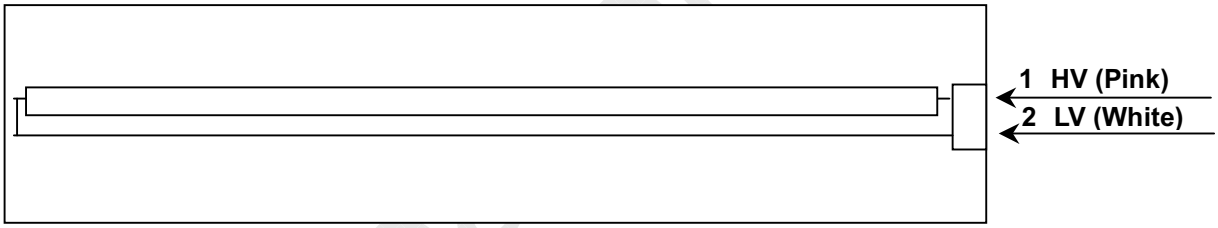
Note (6) The waveform of the voltage output of inverter must be area-symmetric and the design of the inverter must have specifications for the modularized lamp. The performance of the Backlight, such as lifetime or brightness, is greatly influenced by the characteristics of the DC-AC inverter for the lamp. All the parameters of an inverter should be carefully designed to avoid generating too much current leakage from high voltage output of the inverter. When designing or ordering the inverter please make sure that a poor lighting caused by the mismatch of the Backlight and the inverter (miss-lighting, flicker, etc.) never occurs. If the above situation is confirmed, the module should be operated in the same manners when it is installed in your instrument.

4. BLOCK DIAGRAM

4.1 TFT LCD MODULE



4.2 BACKLIGHT UNIT



5. INPUT TERMINAL PIN ASSIGNMENT

5.1 TFT LCD MODULE

Pin	Symbol	Description	Polarity	Remark
1	Vcc	Power Supply +3.3 V		
2	Vcc	Power Supply +3.3 V		-
3	Vss	Ground		-
4	Vss	Ground		-
5	Rxin0-	LVDS Differential Data Input	Negative	R0~R5,G0
6	Rxin0+	LVDS Differential Data Input	Positive	
7	Vss	Ground		-
8	Rxin1-	LVDS Differential Data Input	Negative	G1~G5,B0,B1
9	Rxin1+	LVDS Differential Data Input	Positive	
10	Vss	Ground		-
11	Rxin2-	LVDS Differential Data Input	Negative	B2~B5,Hsync,Vsync,DE
12	Rxin2+	LVDS Differential Data Input	Positive	
13	Vss	Ground		-
14	CLK-	LVDS Clock Data Input	Negative	LVDS Level
15	CLK+	LVDS Clock Data Input	Positive	
16	Vss	Ground		-
17	V_{EDID}	DDC +3.3 V		
18	NC	Non-Connection		
19	CLK_{EDID}	DDC Clock		-
20	$Data_{EDID}$	DDC Data		-

Note (1) Connector Part No.: JAE-FI-SEB20P-HF10 or equivalent

Note (2) User's connector Part No: JAE-FI-S20S or JAE-FI-SE20M or equivalent

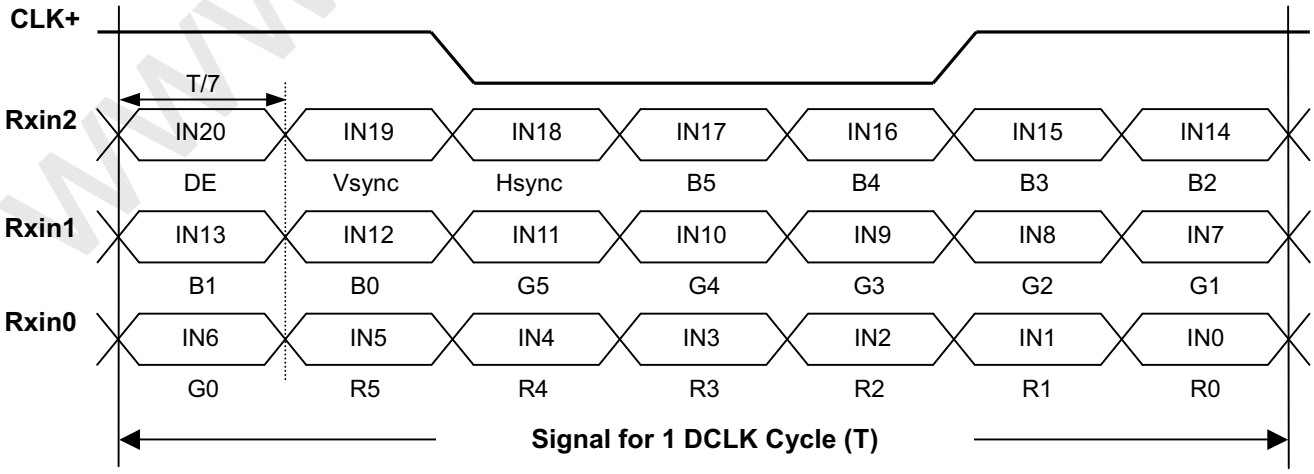
5.2 BACKLIGHT UNIT

Pin	Symbol	Description	Color
1	HV	High Voltage	Pink
2	LV	Ground	White

Note (1) Connector Part No.: JST-BHSR-02VS-1 or equivalent

Note (2) User's connector Part No.: JST-SM02B-BHSS-1-TB or equivalent

5.3 TIMING DIAGRAM OF LVDS INPUT SIGNAL



5.4 COLOR DATA INPUT ASSIGNMENT

The brightness of each primary color (red, green and blue) is based on the 6-bit gray scale data input for the color. The higher the binary input the brighter the color. The table below provides the assignment of color versus data input.

Color		Data Signal																	
		Red						Green						Blue					
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale Of Red	Red(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(61)	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale Of Green	Green(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(61)	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
	Green(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Gray Scale Of Blue	Blue(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(61)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

Note (1) 0: Low Level Voltage, 1: High Level Voltage

5.5 EDID DATA STRUCTURE

The EDID (Extended Display Identification Data) data formats are to support displays as defined in the VESA Plug & Display and FPDI standards.

Byte # (decimal)	Byte # (hex)	Field Name and Comments	Value (hex)	Value (binary)
0	0	Header	00	00000000
1	1	Header	FF	11111111
2	2	Header	FF	11111111
3	3	Header	FF	11111111
4	4	Header	FF	11111111
5	5	Header	FF	11111111
6	6	Header	FF	11111111
7	7	Header	00	00000000
8	8	EISA ID manufacturer name (“FJ”)	06	00000110
9	9	EISA ID manufacturer name (Compressed ASCII)	10	00010000
10	0A	ID product code (N141X5 = ”39963”)	1B	00011011
11	0B	ID product code (hex LSB first)	9C	10011100
12	0C	ID S/N (fixed “0”)	00	00000000
13	0D	ID S/N (fixed “0”)	00	00000000
14	0E	ID S/N (fixed “0”)	00	00000000
15	0F	ID S/N (fixed “0”)	00	00000000
16	10	Week of manufacture (fixed “4”)	00	00000000
17	11	Year of manufacture (fixed “2002”)	00	00000000
18	12	EDID structure version # (“1”)	01	00000001
19	13	EDID revision # (“3”)	03	00000011
20	14	Video I/P definition (“digital”)	80	10000000
21	15	Max H image size (“28 cm”)	1C	00011100
22	16	Max V image size (“21 cm”)	15	00010101
23	17	Display Gamma (Gamma = ”2.2”)	78	01111000
24	18	Feature support (“RGB, preferred timing”)	0A	00001010
25	19	Red/Green (Rx1, Rx0, Ry1, Ry0, Gx1, Gx0, Gy1, Gy0)	8A	10001010
26	1A	Blue/White (Bx1, Bx0, By1, By0, Wx1, Wx0, Wy1, Wy0)	A5	10100101
27	1B	Red-x (Rx = “0.585”)	95	10010101
28	1C	Red-y (Ry = “0.355”)	5B	01011011
29	1D	Green-x (Gx = ”0.299”)	4C	01001100
30	1E	Green-y (Gy = ”0.584”)	95	10010101
31	1F	Blue-x (Bx = ”0.154”)	27	00100111
32	20	Blue-y (By = ”0.135”)	22	00100010
33	21	White-x (Wx = ”0.313”)	50	01010000
34	22	White-y (Wy = ”0.329”)	54	01010100
35	23	Established timings 1	00	00000000
36	24	Established timings 2 (1024x768@60Hz)	08	00001000
37	25	Manufacturer’s reserved timings	00	00000000
38	26	Standard timing ID # 1	01	00000001
39	27	Standard timing ID # 1	01	00000001
40	28	Standard timing ID # 2	01	00000001
41	29	Standard timing ID # 2	01	00000001

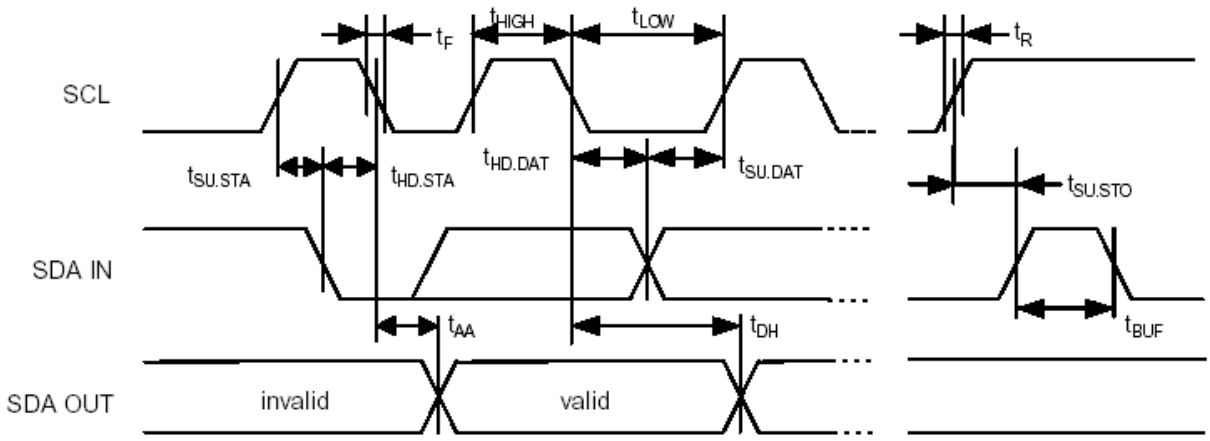
Byte # (decimal)	Byte # (hex)	Field Name and Comments	Value (hex)	Value (binary)
42	2A	Standard timing ID # 3	01	00000001
43	2B	Standard timing ID # 3	01	00000001
44	2C	Standard timing ID # 4	01	00000001
45	2D	Standard timing ID # 4	01	00000001
46	2E	Standard timing ID # 5	01	00000001
47	2F	Standard timing ID # 5	01	00000001
48	30	Standard timing ID # 6	01	00000001
49	31	Standard timing ID # 6	01	00000001
50	32	Standard timing ID # 7	01	00000001
51	33	Standard timing ID # 7	01	00000001
52	34	Standard timing ID # 8	01	00000001
53	35	Standard timing ID # 8	01	00000001
54	36	Detailed timing description # 1 Pixel clock (“65 MHz”)	64	01100100
55	37	# 1 Pixel clock (hex LSB first)	19	00011001
56	38	# 1 H active (“1024”)	00	00000000
57	39	# 1 H blank (“320”)	40	01000000
58	3A	# 1 H active: H blank (“1024 : 320”)	41	01000001
59	3B	# 1 V active (”768”)	00	00000000
60	3C	# 1 V blank (”38”)	26	00100110
61	3D	# 1 V active: V blank (”768 : 38”)	30	00110000
62	3E	# 1 H sync offset (”24”)	18	00011000
63	3F	# 1 H sync pulse width (”136”)	88	10001000
64	40	# 1 V sync offset: V sync pulse width (”3 : 6”)	36	00110110
65	41	# 1 H sync offset: H sync pulse width : V sync offset : V sync width (”24 : 136 : 3 : 6”)	00	00000000
66	42	# 1 H image size (”285 mm”)	1D	00011101
67	43	# 1 V image size (”214 mm”)	D6	11010110
68	44	# 1 H image size: V image size (”285 : 214”)	10	00010000
69	45	# 1 H boarder (”0”)	00	00000000
70	46	# 1 V boarder (”0”)	00	00000000
71	47	# 1 Flags (”Non-Interlace, Non-Stereo, Digital Separate”)	18	00011000
72	48	Detailed timing description # 2	00	00000000
73	49	# 2 Flag	00	00000000
74	4A	# 2 Reserved	00	00000000
75	4B	# 2 FE (hex) defines ASCII string (Model Name “N141X5”, ASCII)	FE	11111110
76	4C	# 2 Flag	00	00000000
77	4D	# 2 1st character of string (“N”)	4E	01001110
78	4E	# 2 2nd character of string (“1”)	31	00110001
79	4F	# 2 3rd character of string (“4”)	34	00110100
80	50	# 2 4th character of string (“1”)	31	00110001
81	51	# 2 5th character of string (“X”)	58	01011000
82	52	# 2 6th character of string (“5”)	35	00110101
83	53	# 2 New line character # 2 indicates end of ASCII string	0A	00001010
84	54	# 2 Padding with “Blank” character	20	00100000
85	55	# 2 Padding with “Blank” character	20	00100000
86	56	# 2 Padding with “Blank” character	20	00100000

Byte # (decimal)	Byte # (hex)	Field Name and Comments	Value (hex)	Value (binary)
87	57	# 2 Padding with "Blank" character	20	00100000
88	58	# 2 Padding with "Blank" character	20	00100000
89	59	# 2 Padding with "Blank" character	20	00100000
90	5A	Detailed timing description # 3	00	00000000
91	5B	# 3 Flag	00	00000000
92	5C	# 3 Reserved	00	00000000
93	5D	# 3 FE (hex) defines ASCII string (Model Name "N141X5", ASCII)	FE	11111110
94	5E	# 3 Flag	00	00000000
95	5F	# 3 1st character of string ("N")	4E	01001110
96	60	# 3 2nd character of string ("1")	31	00110001
97	61	# 3 3rd character of string ("4")	34	00110100
98	62	# 3 4th character of string ("1")	31	00110001
99	63	# 3 5th character of string ("X")	58	01011000
100	64	# 3 6th character of string ("5")	35	00110101
101	65	# 3 New line character # 3 indicates end of ASCII string	0A	00001010
102	66	# 3 Padding with "Blank" character	20	00100000
103	67	# 3 Padding with "Blank" character	20	00100000
104	68	# 3 Padding with "Blank" character	20	00100000
105	69	# 3 Padding with "Blank" character	20	00100000
106	6A	# 3 Padding with "Blank" character	20	00100000
107	6B	# 3 Padding with "Blank" character	20	00100000
108	6C	Detailed timing description # 4	00	00000000
109	6D	# 4 Flag	00	00000000
110	6E	# 4 Reserved	00	00000000
111	6F	# 4 FC (hex) defines Monitor name ("Color LCD", ASCII)	FC	11111100
112	70	# 4 Flag	00	00000000
113	71	# 4 1st character of name ("C")	43	01000011
114	72	# 4 2nd character of name ("o")	6F	01101111
115	73	# 4 3rd character of name ("l")	6C	01101100
116	74	# 4 4th character of name ("o")	6F	01101111
117	75	# 4 5th character of name ("r")	72	01110010
118	76	# 4 6th character of name (<space>)	20	00100000
119	77	# 4 7th character of name ("L")	4C	01001100
120	78	# 4 8th character of name ("C")	43	01000011
121	79	# 4 9th character of name ("D")	44	01000100
122	7A	# 4 New line character # 4 indicates end of Monitor name	0A	00001010
123	7B	# 4 Padding with "Blank" character	20	00100000
124	7C	# 4 Padding with "Blank" character	20	00100000
125	7D	# 4 Padding with "Blank" character	20	00100000
126	7E	Extension flag	00	00000000
127	7F	Checksum	EE	11101110

5.6 EDID SIGINAL SPECIFICATION

(1) EDID Power

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Power supply voltage	Vcc	Read Operation	2.2	—	5.5	V



(2) DC characteristics

		Symbol	Min.	Max.	Unit	Index
SCL, SDA terminal input voltage	High Voltage	VIH	0.7×Vcc	—	V	
	Low Voltage	VIL	—	0.3×Vcc	V	
Hysteresis Voltage		VHYS	0.05 VCC	—	V	
Output Voltage		VOL1 VOL2	—	0.4 0.6	V	IOL=3mA, CC=2.5V IOL=6mA, CC=2.5V
Input Leak current (Vin =0.1V~VCC)		ILI	-10 -10	10 50	uA	WP=VSS WP=VCC
Output Leak current		ILO	-10	10	uA	Vout =0.1V~VCC, WP=VSS
Terminal capacity(Input, Output)		Cin, Cout	—	10	pF	VCC=5.0V Ta=25^{0}C, Fclk=1.0MHz
Operating current		ICC Write ICC Read	—	3 1	mA	VCC=5.5V, SCL=400KHz
Stillness current (SDA=SCL=VCC) (WP=VSS,A0,A1,A2=VSS)		ICCS	—	30 100	uA	VCC=3.0V VCC=5.5V

(3) AC characteristics (VCC=2.5~5.5V standard operation mode)

Item	Symbol	VCC=2.5V-5.5V (Standard operation mode)		VCC=4.5V-5.5V (High-speed operation mode)			
		Min.	Max.	Min.	Max.	Unit	Index
Clock frequency	Fclk	—	100	—	400	KHz	
Clock High Time	THIGH	4000	—	900	—	ns	
Clock Low Time	TLOW	4700	—	1300	—	ns	
SDA, SCL falling time	TR	—	1000	—	300	ns	
SDA, SCL rising time	TF	—	300	—	300	ns	
START hold time	THD: STA	4000	—	600	—	ns	
START setup time	TSU: STA	4700	—	600	—	ns	
Data input hold time	THD: Data	0	—	0	—	ns	
Data input setup time	TSU: Data	250	—	100	—	ns	
STOP setup time	TSU: STO	4700	—	600	—	ns	
Output decision time from a clock	TAA	—	3500	100	900	ns	
Bus free time	TBUF	4700	—	1300	—	ns	
Rising time of Min VIH, VIL	TOF	—	250	20	250	ns	CB≦100pF
Spike oppression	TSP	—	50	—	50	ns	
A write-in cycle time	TWR	—	10	—	10	ms	Byte and page mode
The number of times of data rewriting	—	1M	—	1M	—	cycles	VCC=5.0V Ta=25^{0}C,

6. INTERFACE TIMING

6.1 INPUT SIGNAL TIMING SPECIFICATIONS

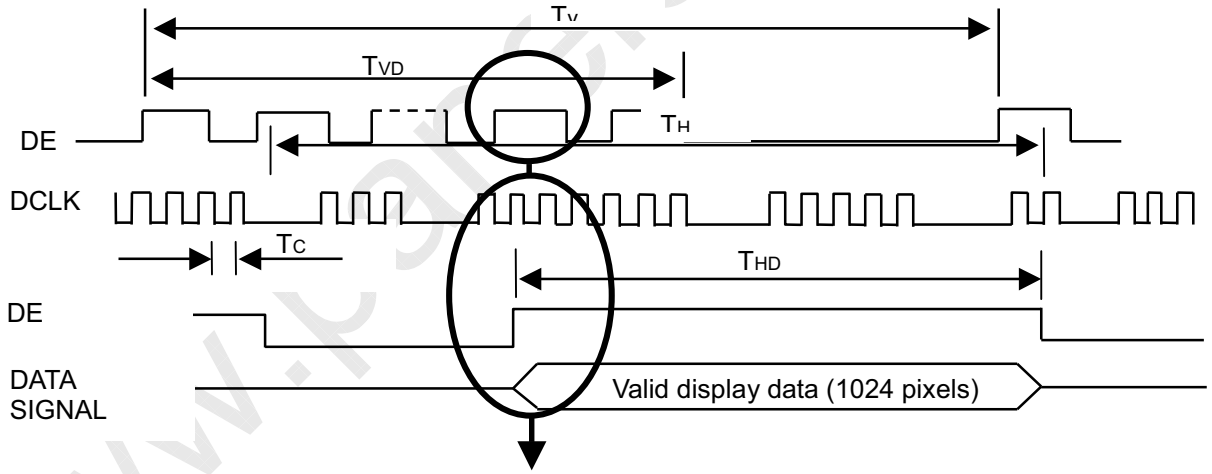
The input signal timing specifications are shown as the following table and timing diagram.

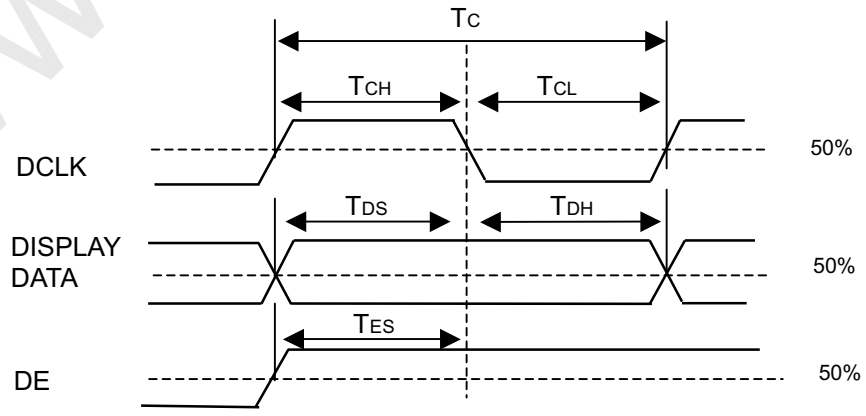
Signal	Item	Symbol	Min.	Typ.	Max.	Unit	Note
Clock	Frequency	1/Tc	-	65	80	MHz	-
	High Time	TCH	13	-	-	nsec	-
	Low Time	TCL	13	-	-	nsec	-
Data	Setup Time	TDS	4	-	-	nsec	-
	Hold Time	TDH	4	-	-	nsec	-
Vsync Frequency	Frequency	Vsync			70	Hz	
Hsync Frequency	Frequency	Hsync			56	KHz	
Data Enable	Pulse width	TDEP	100		-	clocks	(1)
Data Enable	Setup Time	TES	3.5	4.0	-	nsec	(1)
Frame Frequency	Cycle	TV	770	806	1000	lines	-
Vertical Active Display Term	Display Period	TVD	-	768	-	lines	-
One Line Scanning Time	Cycle	TH	1180	1344	2000	clocks	(2)
Horizontal Active Display Term	Display Period	THD	-	1024	-	clocks	-

Note (1) Because this module is operated by DE only mode, Hsync and Vsync input signals should be set to low logic level or ground. Otherwise, this module would operate abnormally.

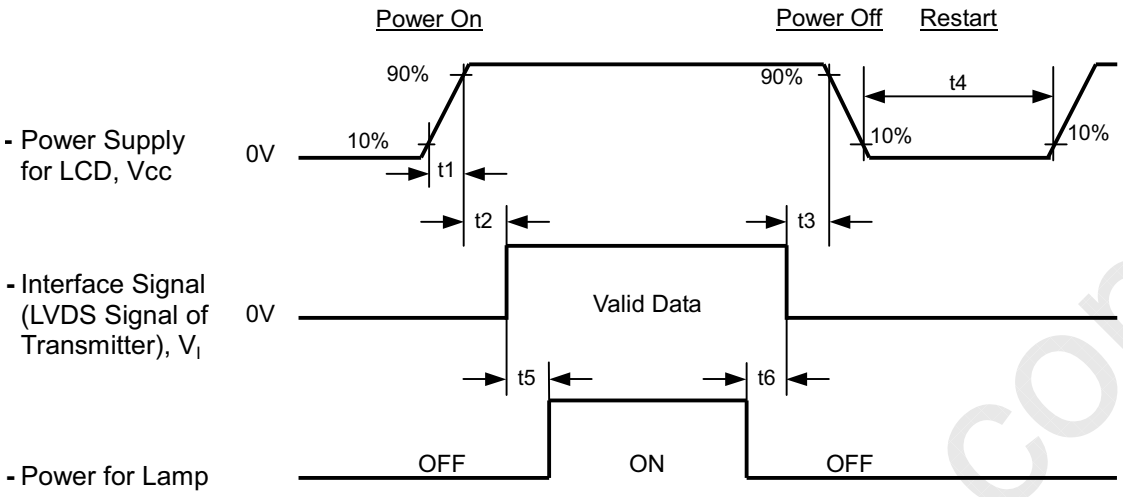
Note (2) The duration of DE signal must be longer than 1 clock period at every horizontal sync. period.

INPUT SIGNAL TIMING DIAGRAM





6.2 POWER ON/OFF SEQUENCE



Timing Specifications:

0 < t1 ≦ 15 msec

0 < t2 ≦ 45 msec

0 < t3 ≦ 45 msec

t4 ≧ 400 msec

t5 ≧ 200 msec

t6 ≧ 200 msec

Note (1) Please avoid floating state of interface signal at invalid period.

Note (2) When the interface signal is invalid, be sure to pull down the power supply of LCD Vcc to 0 V.

Note (3) The Backlight inverter power must be turned on after the power supply for the logic and the interface signal is valid. The Backlight inverter power must be turned off before the power supply for the logic and the interface signal is invalid.

7. OPTICAL CHARACTERISTICS

7.1 TEST CONDITIONS

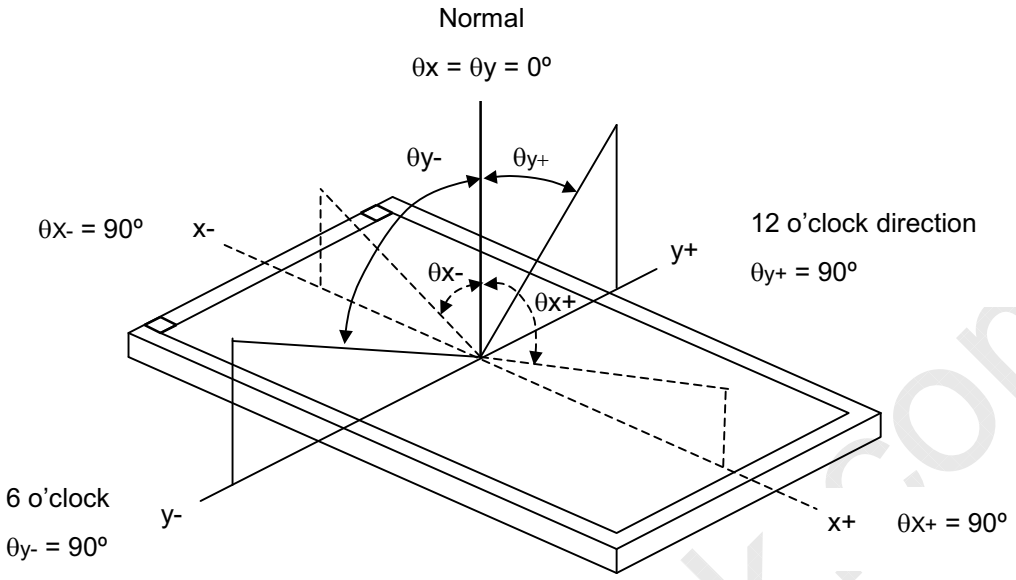
Item	Symbol	Value	Unit
Ambient Temperature	Ta	25±2	°C
Ambient Humidity	Ha	50±10	%RH
Supply Voltage	V_{CC}	3.3	V
Input Signal	According to typical value in "3. ELECTRICAL CHARACTERISTICS"		
Inverter Current	I_L	6.0	mA
Inverter Driving Frequency	F_L	55	KHz
Inverter	Sumida-H05-4783B		

The measurement methods of optical characteristics are shown in Section 7.2. The following items should be measured under the test conditions described in Section 7.1 and stable environment shown in Note (6).

7.2 OPTICAL SPECIFICATIONS

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Contrast Ratio		CR	θ_x=0°, θ_Y =0° Viewing Normal Angle	150	200	-	-	(2), (6)
Response Time		T_R		-	15	30	ms	(3)
		T_F		-	35	50	ms	
Center Luminance of White		L		180	210	-	cd/m^2	(6)
Average Luminance of White		L_{AVE}		170	200	-	cd/m^2	(4), (6)
White Variation		δW		-	-	1.4	-	(6), (7)
Cross Talk		CT		-	-	3.0	%	(5), (6)
Color Chromaticity	Red	Rx		0.555	0.585	0.615	-	(1), (6)
		Ry		0.325	0.355	0.385	-	
	Green	Gx		0.269	0.299	0.329	-	
		Gy		0.554	0.584	0.614	-	
	Blue	Bx		0.124	0.154	0.184	-	
		By		0.105	0.135	0.165	-	
	White	Wx		0.283	0.313	0.343	-	
		Wy		0.299	0.329	0.359	-	
	Color Gamut	C.G%		45	50	-	%	(8)
Viewing Angle	Horizontal	θ_x+	CR≥10	40	45	-	Deg.	(1), (6)
		θ_x-		40	45	-		
	Vertical	θ_Y+		10	15	-		
		θ_Y-		30	35	-		

Note (1) Definition of Viewing Angle (θx, θy):



Note (2) Definition of Contrast Ratio (CR):

The contrast ratio can be calculated by the following expression.

Contrast Ratio (CR) = L63 / L0

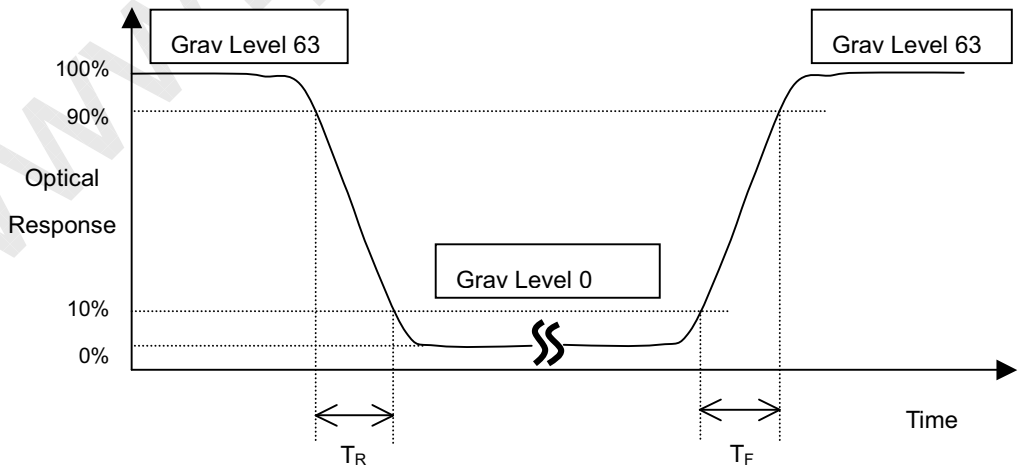
L63: Luminance of gray level 63

L 0: Luminance of gray level 0

CR = CR (5)

CR (X) is corresponding to the Contrast Ratio of the point X at Figure in Note (7).

Note (3) Definition of Response Time (T_R, T_F):



Note (4) Definition of Average Luminance of White (L_{AVE}):

Measure the luminance of gray level 63 at 5 points

L_{AVE} = [L (1)+ L (2)+ L (3)+ L (4)+ L (5)] / 5

L (x) is corresponding to the luminance of the point X at Figure in Note (7).

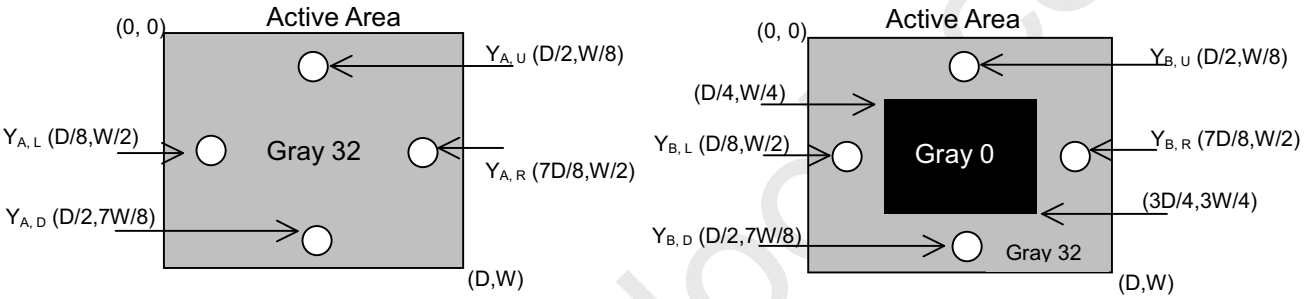
Note (5) Definition of Cross Talk (CT):

$CT = | Y_B – Y_A | / Y_A \times 100$ (%)

Where:

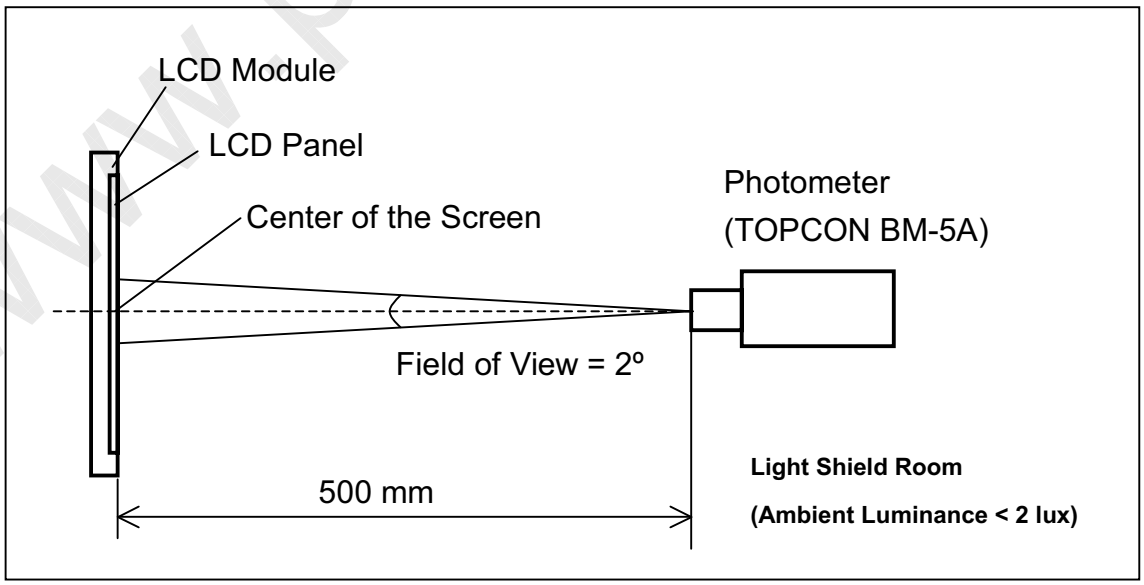
Y_A = Luminance of measured location without gray level 0 pattern (cd/m^2)

Y_B = Luminance of measured location with gray level 0 pattern (cd/m^2)



Note (6) Measurement Setup:

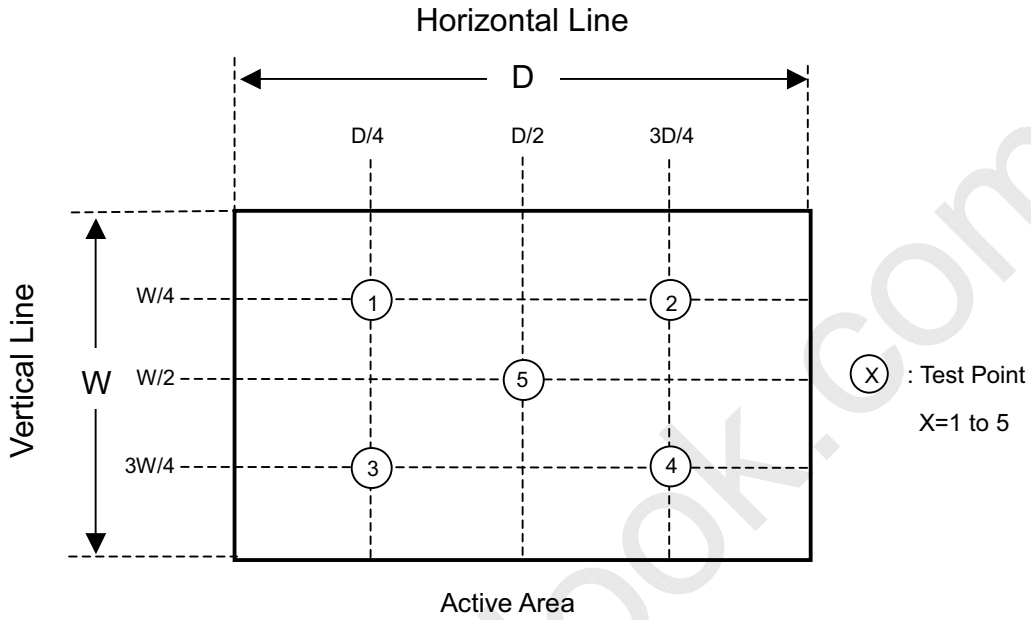
The LCD module should be stabilized at given temperature for 20 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 20 minutes in a windless room.



Note (7) Definition of White Variation (δW):

Measure the luminance of gray level 63 at 5 points

δW = Maximum [L (1), L (2), L (3), L (4), L (5)] / Minimum [L (1), L (2), L (3), L (4), L (5)]



Note (8) Definition of color gamut (C.G%):

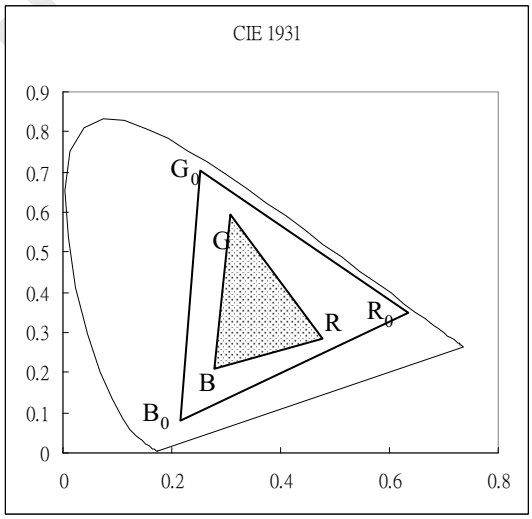
C.G%= ΔR G B /ΔR_0 G_0 B_0 ×100%

R_0, G_0, B_0 : color coordinates of red, green, and blue defined by NTSC, respectively.

R, G, B : color coordinates of module on 63 gray levels of red, green, and blue, respectively.

ΔR_0 G_0 B_0 : area of triangle defined by R_0, G_0, B_0

ΔR G B: area of triangle defined by R, G, B



8. PRECAUTIONS

8.1 HANDLING PRECAUTIONS

(1) The module should be assembled into the system firmly by using every mounting hole. Be careful not to twist or bend the module.

(2) While assembling or installing modules, it can only be in the clean area. The dust and oil may cause electrical short or damage the polarizer.

(3) Use fingerstalls or soft gloves in order to keep display clean during the incoming inspection and assembly process.

(4) Do not press or scratch the surface harder than a HB pencil lead on the panel because the polarizer is very soft and easily scratched.

(5) If the surface of the polarizer is dirty, please clean it by some absorbent cotton or soft cloth. Do not use Ketone type materials (ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanently damage the polarizer due to chemical reaction.

(6) Wipe off water droplets or oil immediately. Staining and discoloration may occur if they left on panel for a long time.

(7) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contacting with hands, legs or clothes, it must be washed away thoroughly with soap.

(8) Protect the module from static electricity, it may cause damage to the C-MOS Gate Array IC.

(9) Do not disassemble the module.

(10) Do not pull or fold the lamp wire.

(11) Pins of I/F connector should not be touched directly with bare hands.

8.2 STORAGE PRECAUTIONS

(1) High temperature or humidity may reduce the performance of module. Please store LCD module within the specified storage conditions.

(2) It is dangerous that moisture come into or contacted the LCD module, because the moisture may damage LCD module when it is operating.

(3) It may reduce the display quality if the ambient temperature is lower than 10 ºC. For example, the response time will become slowly, and the starting voltage of lamp will be higher than the room temperature.

8.3 OPERATION PRECAUTIONS

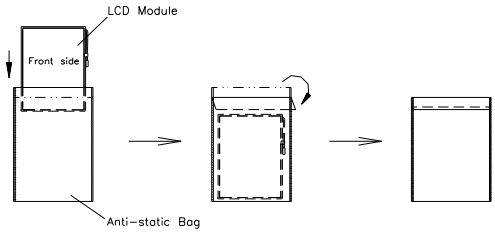
(1) Do not pull the I/F connector in or out while the module is operating.

(2) Always follow the correct power on/off sequence when LCD module is connecting and operating. This can prevent the CMOS LSI chips from damage during latch-up.

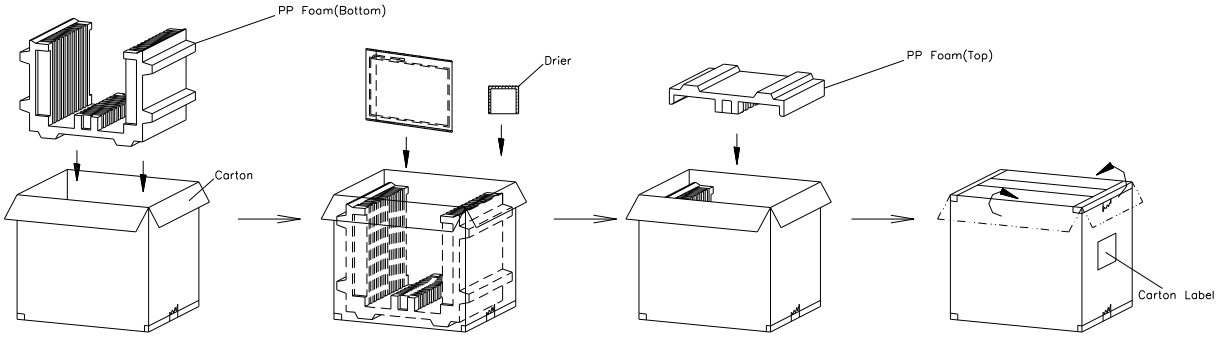
(3) The startup voltage of Backlight is approximately 1000 Volts. It may cause electrical shock while assembling with inverter. Do not disassemble the module or insert anything into the Backlight unit.

9. PACKING

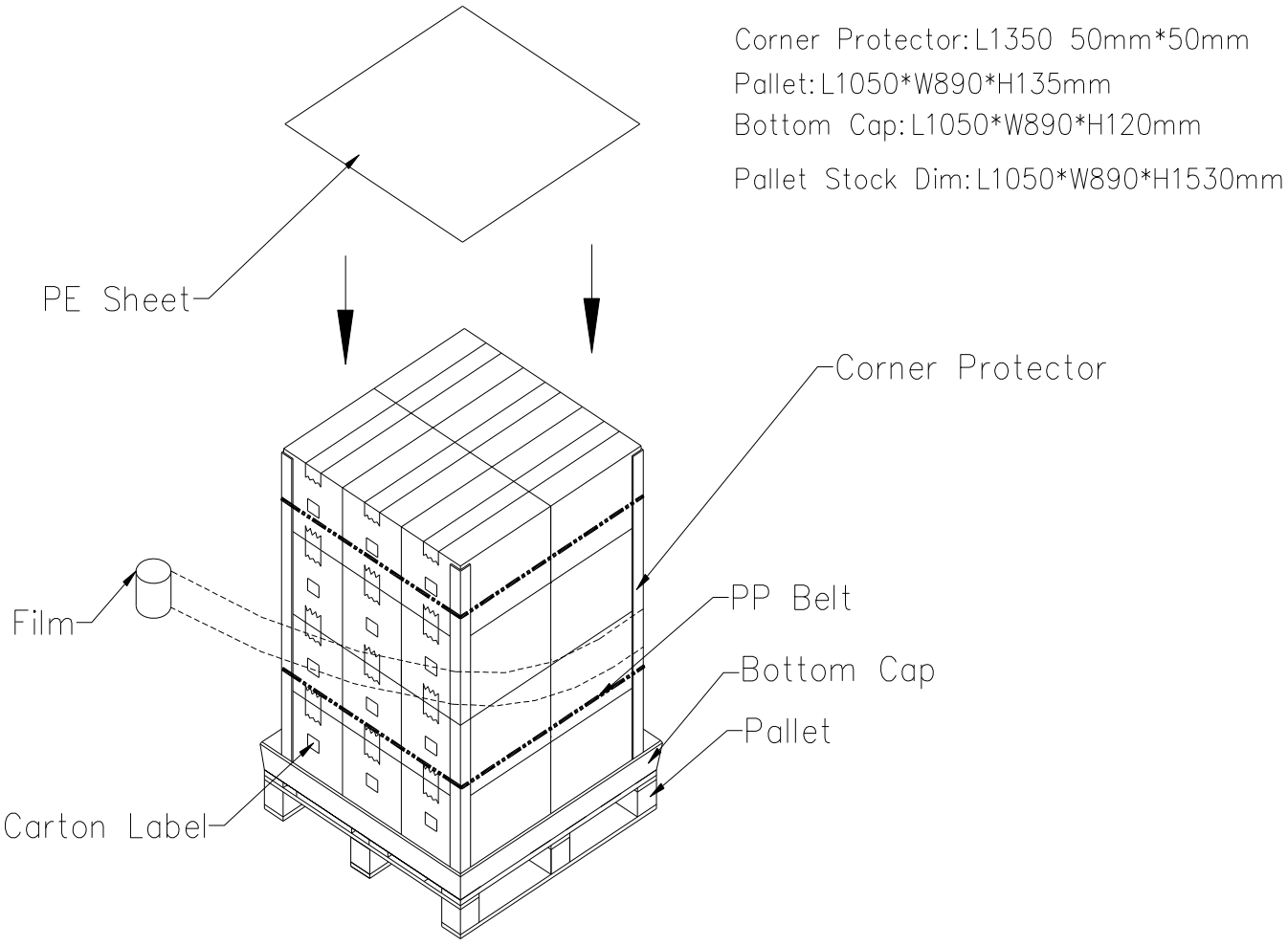
9.1 CARTON



Box dimensions: 422(L)x337(W)x345(H)mm
Weight: Approx. 7.0Kg(10modules per 1 box)



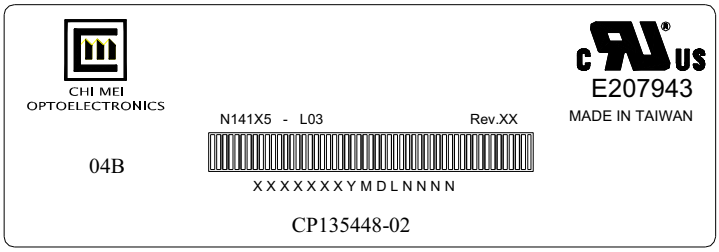
9.2 PALLET



10. DEFINITION OF LABELS

10.1 CMO MODULE LABEL

The barcode nameplate is pasted on each module as illustration, and its definitions are as following explanation.



(a) Model Name: N141X5 - L03

(b) Revision: Rev. XX, for example: C1, C2 …etc.

(c) Serial ID: X X X X X X Y M D L N N N N

- Serial No.
- Product Line
- Year, Month, Date
- CMO Internal Use
- Revision
- CMO Internal Use

(d) Customer Internal Product Code : CP135448-02

(e) Customer Internal Revision : XXX, for example: 01A, 02A …etc

Serial ID includes the information as below:

(a) Manufactured Date: Year: 1~9, for 2001~2009

Month: 1~9, A~C, for Jan. ~ Dec.

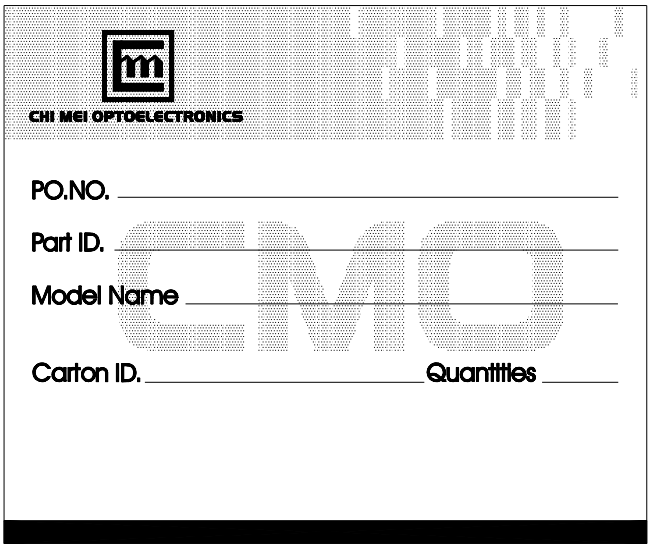
Day: 1~9, A~Y, for 1st to 31st, exclude I , O and U

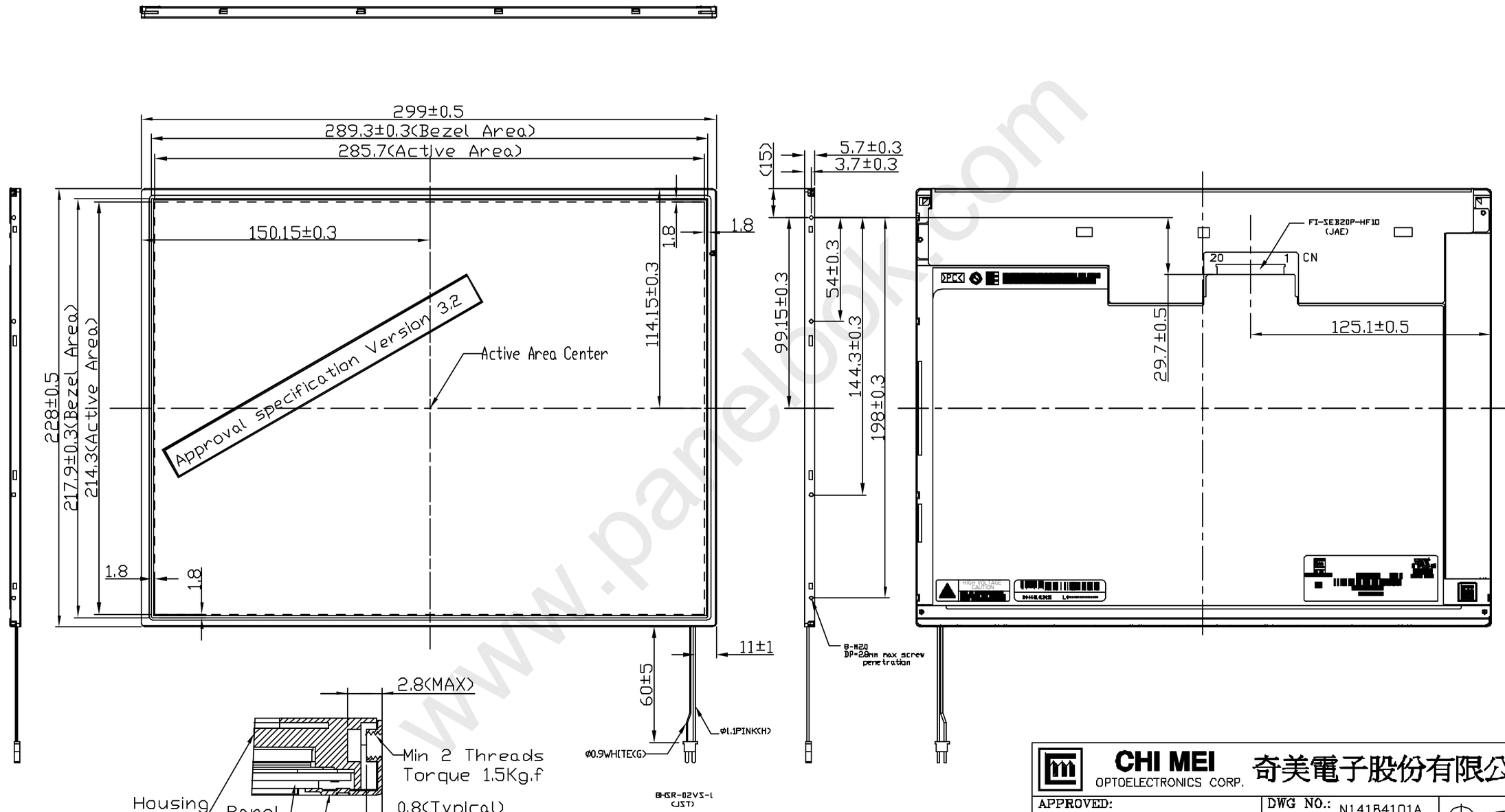
(b) Revision Code: cover all the change

(c) Serial No.: Manufacturing sequence of product

(d) Product Line: 1 -> Line1, 2 -> Line 2, …etc.

10.2 CARTON LABEL





Structure of Joint

NOTE:
1.GENERAL TOLERANCE:±0.3
2.SCREW TORQUE FOR MOUNTING SHALL NOT EXCEED 1.5Kgf-cm

MARK	DESCRIPTION	DATE	CHANGE BY	APPROVAL BY	ECN NO.

CHI MEI OPTOELECTRONICS CORP. 奇美電子股份有限公司

APPROVED:		DWG NO.: N141B4101A			
CHECKED:		DATE: 18/FEB/2002			
DESIGNER: YULE LIN		PART NO.:			
DRAWER: YULE LIN	REV.: A	UNIT:	SCALE:	SHEET:	
TITLE: OUTLINE N141X5-L03		MM		1/1	